

Features

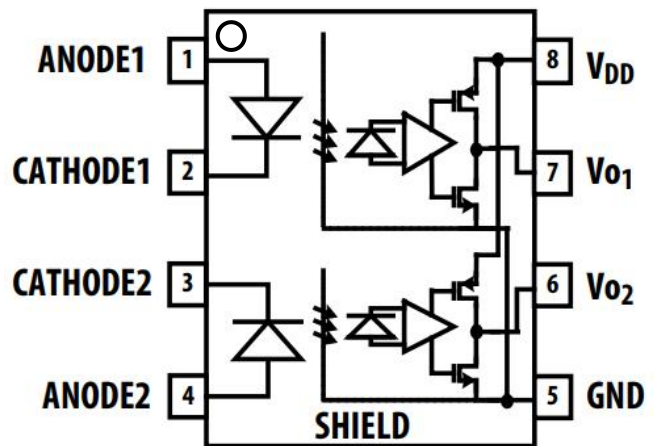
- +3.3V and +5V CMOS compatibility
- 40 ns max. pulse width distortion
- 30 ns max. propagation delay skew
- High speed: 15 MBd min.
- 10 kV/μs minimum common mode rejection
- -40°C to 105°C temperature range
- Regulatory Approvals:
 - UL - UL1577
 - VDE - EN60747-5-5(VDE0884-5)
 - CQC - GB4943.1

Applications

- Digital field bus isolation:
- CANBus, RS485, USB
- Multiplexed data transmission
- Computer peripheral interface
- Microprocessor system interface
- DC/DC converter

Description

ICPL-075L (dual-channel) are 15MBd CMOS optocouplers in SOIC-8 package. The optocouplers use the latest CMOS IC technology to achieve outstanding performance with very low power consumption. Basic building blocks of ICPL-075L are high speed LEDs and CMOS detector ICs. Each detector incorporates an integrated photodiode, a high-speed transimpedance amplifier, and a voltage comparator with an output driver.




Truth Table

LED	OUTPUT
OFF	H
ON	L



ORDERING INFORMATION

Outline	Part Number	Package	Marking	Packing	Packing Size	Quantity
	ICPL-075L-500E	SOP8	ICPL 075L /YYWW B	Reel	13 "	2000

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PIN CONFIGURATION AND FUNCTIONS

Pin	Name
1	ANODE1
2	CATHODE1
3	CATHODE2
4	ANODE2
5	GND
6	V _{O2}
7	V _{O1}
8	V _{DD}

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Storage Temperature	T _S	-55	+125	°C
Ambient Operating Temperature	T _a	-40	+105	°C
Supply Voltages	V _{DD}	0	6.0	Volts
Output Voltage	V _O	-0.5	V _{DD} +0.5	Volts
Average Forward Input Current	I _F	-	20.0	mA
Average Output Current	I _O	-	10.0	mA
Input Power Dissipation	P _I	-	35	mW
Output Power Dissipation	P _O	-	100	mW
Lead Solder Temperature	260°C for 10s, 1.6 mm below seating plane			
Solder Reflow Temperature Profile	See Reflow Soldering Profile			

RECOMMENDED OPERATION CONDITIONS

Parameter	Symbol	Min.	Max.	Unit
Operating Temperature	T _a	-40	105	°C
Supply Voltage	V _{DD}	4.5	5.5	V
	V _{DD}	3.0	3.6	V
Input Current(ON)	I _F	9	18	mA
Supply Voltage Slew Rate	S _R	0.5	500	V/ms

ELECTRICAL OPTICAL CHARACTERISTICS (T_a=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition	Note
INPUT							
Forward Voltage	V _F	1.3	1.5	1.8	V	I _F =14mA	
Input Reverse Breakdown Voltage	BV _R	5.0	35	-	V	I _R =10μA	
OUTPUT							
Logic High Output Voltage	V _{OH}	V _{DD} -1	3.1		V	I _F =0A, I _O =-4mA, V _{DD} =3.3V	
		V _{DD} -1	4.85		V	I _F =0A, I _O = - 4mA, V _{DD} =5.0V	
Logic Low Output Voltage	V _{OL}	-	0.08	0.8	V	I _F =14mA, I _O = 4 mA, V _{DD} = 3.3V	
		-	0.07	0.8	V	I _F =14mA, I _O =4 mA, V _{DD} =5.0V	
Input Threshold Current	I _{TH}	-	3	5	mA	I _{OL} =20μA	
Low Output Supply Current	I _{DDL}	-	9.0	12	mA	I _F =14mA	
High Output Supply Current	I _{DDH}	-	9.4	12	mA	I _F =0mA	

SWITCHING CHARACTERISTICS

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition	Note
Over recommended temperature($T_a = -40^{\circ}\text{C}$ to $+105^{\circ}\text{C}$) $3.0\text{V} \leq V_{DD} \leq 3.6\text{V}$ and $4.5\text{V} \leq V_{DD} \leq 5.5\text{V}$. All typical specifications are at $T_a = +25^{\circ}\text{C}$, $V_{DD} = +3.3\text{V}$.							
Propagation Delay Time to Output Low Level	t_{PHL}	-	28	80	ns	$I_F=14\text{mA}, C_L=15\text{pF}, V_{DD}=3.3\text{V}$	
		-	-	80	ns	$I_F=14\text{mA}, C_L=15\text{pF}, V_{DD}=5\text{V}$	
Propagation Delay Time to Output High Level	t_{PLH}	-	62	80	ns	$I_F=14\text{mA}, C_L=15\text{pF}, V_{DD}=3.3\text{V}$	
		-	-	80	ns	$I_F=14\text{mA}, C_L=15\text{pF}, V_{DD}=5\text{V}$	
Pulse Width	t_{PW}	66.7			ns		
Pulse Width Distortion	$ t_{PHL}-t_{PLH} $	0	34	40	ns	$I_F=14\text{mA}, C_L=15\text{pF}, V_{DD}=3.3\text{V}$	
		-	-	40	ns	$I_F=14\text{mA}, C_L=15\text{pF}, V_{DD}=5\text{V}$	
Propagation Delay Skew	t_{PSK}	-	-	30	ns	$I_F=14\text{mA}, C_L=15\text{pF}$	
Output Rise Time (10% – 90%)	t_r	-	6	-	ns	$I_F=14\text{mA}, C_L=15\text{pF}$	
Output Fall Time (90% - 10%)	t_f	-	4	-	ns	$I_F=14\text{mA}, C_L=15\text{pF}$	
Common Mode Transient Immunity at Logic High Output	$ CM_H $	10	15	-	kV/ μs	$I_F=0\text{mA}, T_a=25^{\circ}\text{C}, V_{CM} =1\text{kV}$	
Common Mode Transient Immunity at Logic Low Output	$ CM_L $	10	15	-	kV/ μs	$I_F=14\text{mA}, T_a=25^{\circ}\text{C}, V_{CM} =1\text{kV}$	
Input-Output Resistance	R_{I-O}	-	10^{12}	-	Ω	$V_{I-I}=500\text{V}, 40\sim 60\% \text{R.H.}$	
Input-Output Capacitance	C_{I-O}	-	0.6	-	pF	$V=0, F=1\text{MHz}$	

CHARACTERISTIC CURVES

Fig.1 Typical Input Diode Forward Characteristic

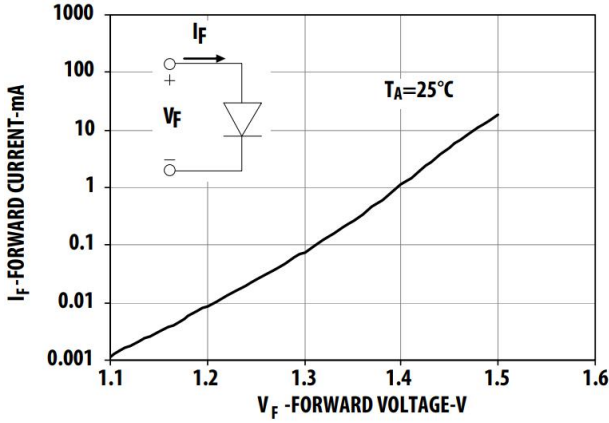


Fig.2 Typical Input Threshold Current vs. Temperature

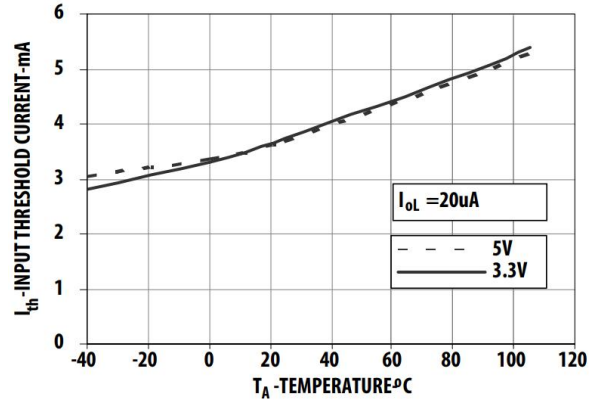


Fig.3 Typical Logic High O/P Supply Current vs. Temperature

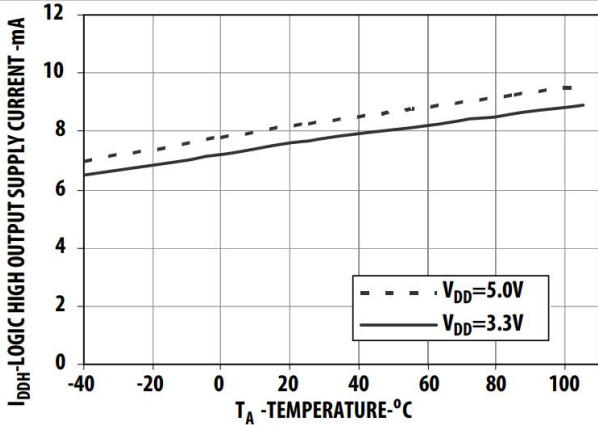


Fig.4 Typical Logic Low O/P Supply Current vs. Temperature

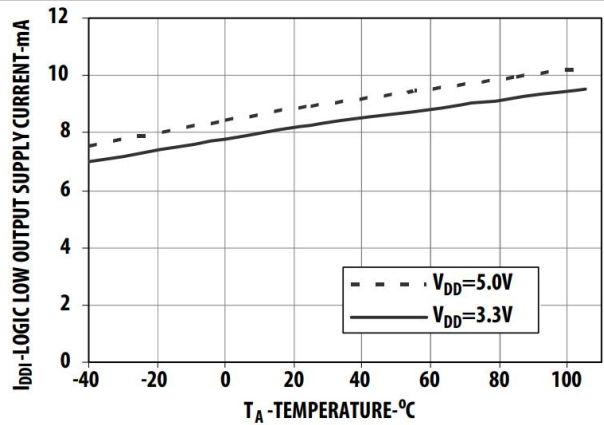


Fig.5 Typical Switching speed vs. Pulse Input Current at 5V Supply Voltage

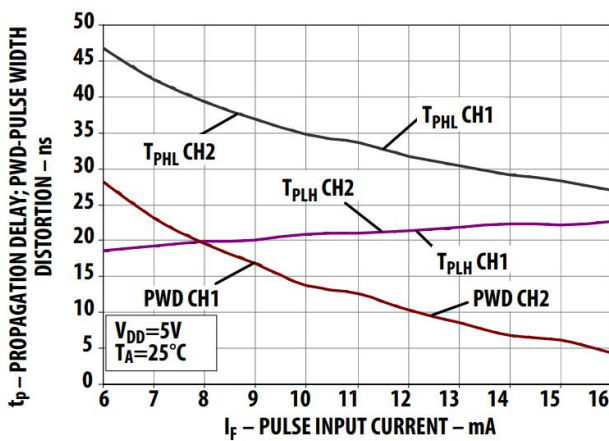
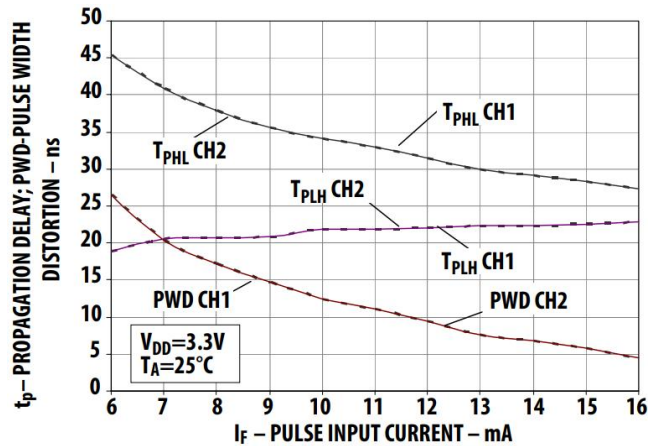
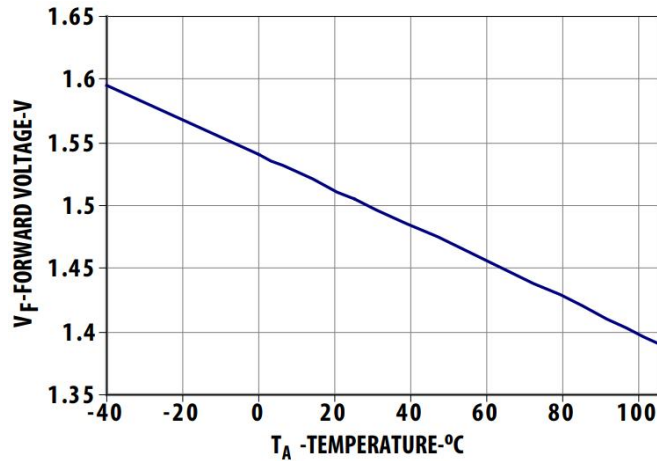


Fig.6 Typical Switching Speed vs. Pulse Input Current at 3.3V Supply Voltage



CHARACTERISTIC CURVES

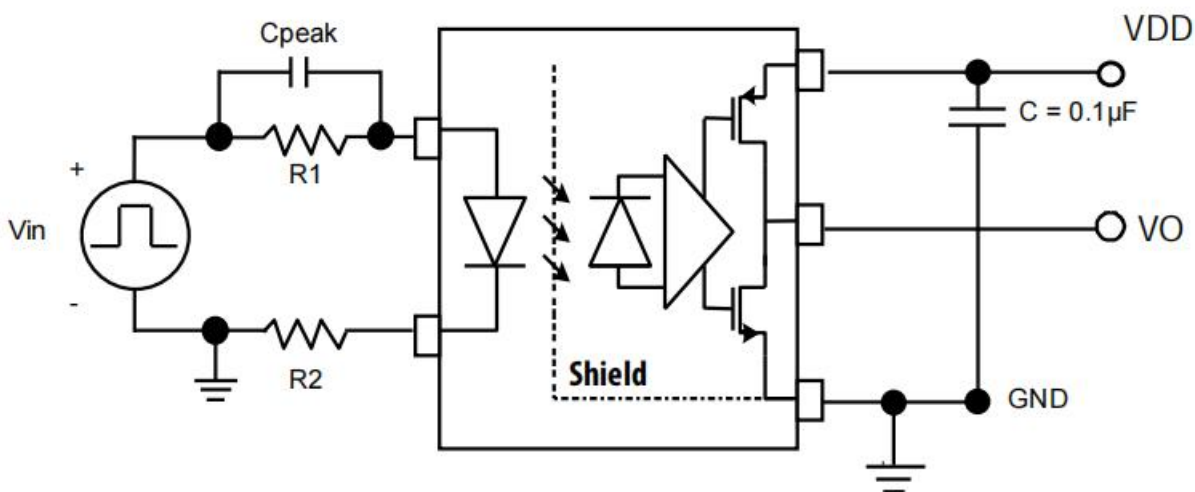
Fig.7 Typical V_F vs. Temperature



Input Limiting Resistors

ICPL-075L is direct current driven (Figure 8), and thus eliminate the need for input power supply. To limit the amount of current flowing through the LED, it is recommended that a 210 Ω resistor is connected in series with anode of LED (that is, Pins 1 and 4 for ICPL-075L) at 5V input signal. At 3.3V input signal, it is recommended to connect a 80 Ω resistor in series with anode of LED. The recommended limiting resistors are based on the assumption that the driver output impedance is 50 (as shown in Figure 9).

Connection of Peaking capacitor (C_{peak}) in Parallel of the Input Limiting Resistor (R_{limit}) to Improve Speed Performance



TEST CIRCUITS

Fig.8 Test circuit for Common Mode Transient Immunity and Typical Waveforms. R_{total} is the total resistance of the driver output impedance (which is assumed to be 50Ω) and the limiting resistor ($R_{total} = R_{drv} + R_{limit}$).

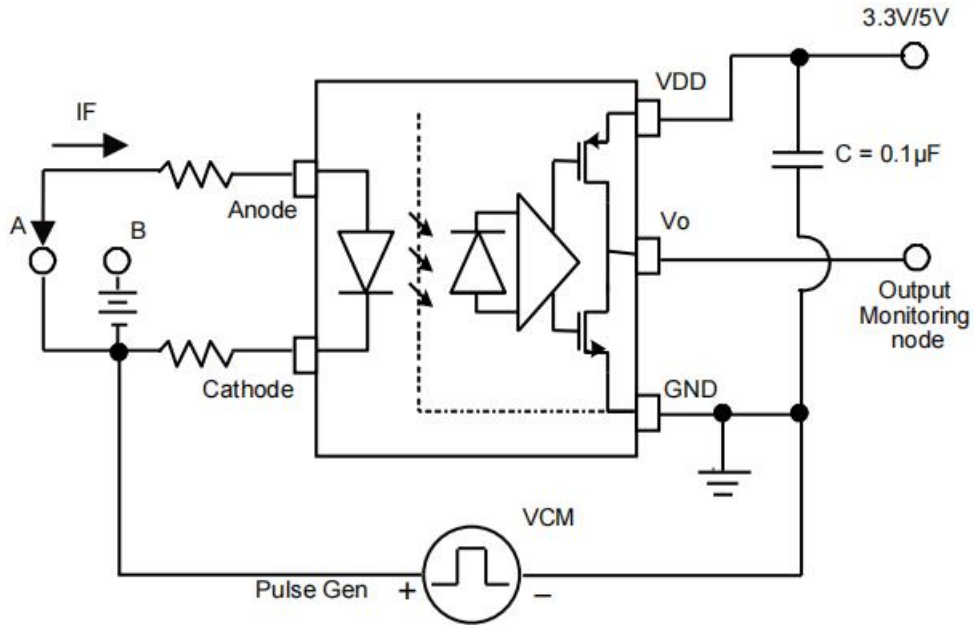
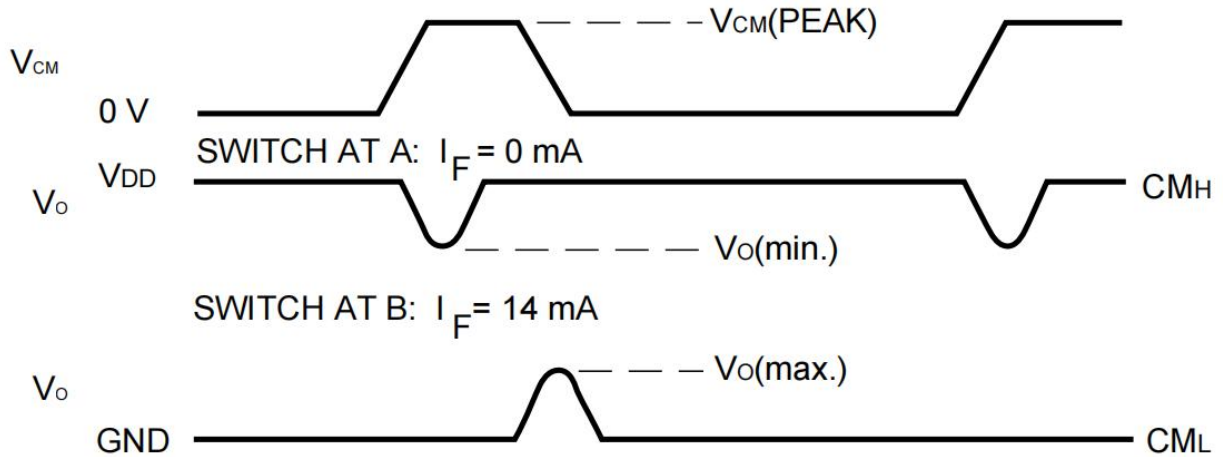
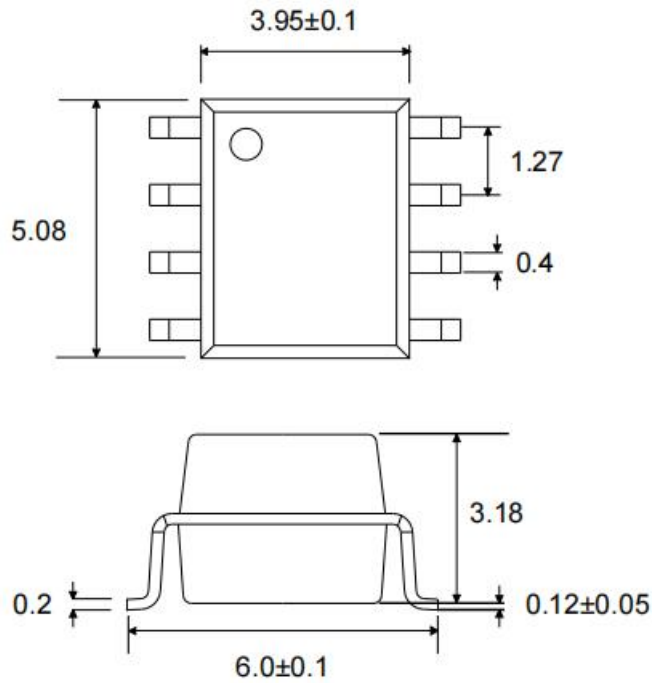


Fig.9 Waveforms of Common Mode Transient Immunity



PACKAGE DIMENSIONS

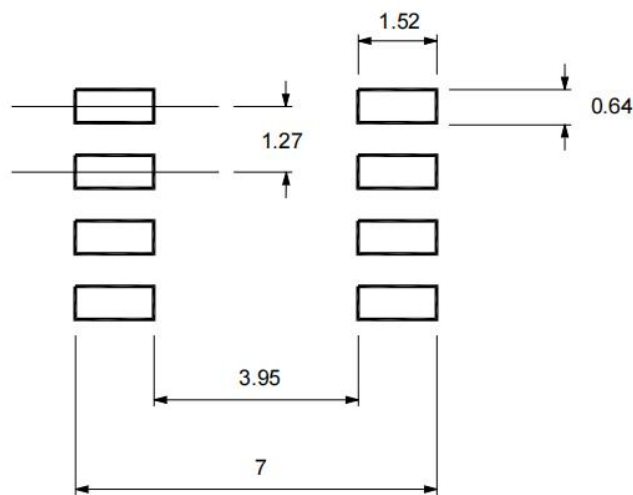
Surface Mount (Low Profile) Lead Forming (SOP8)



- Dimensions in mm unless otherwise stated

RECOMMENDED SOLDER MASK

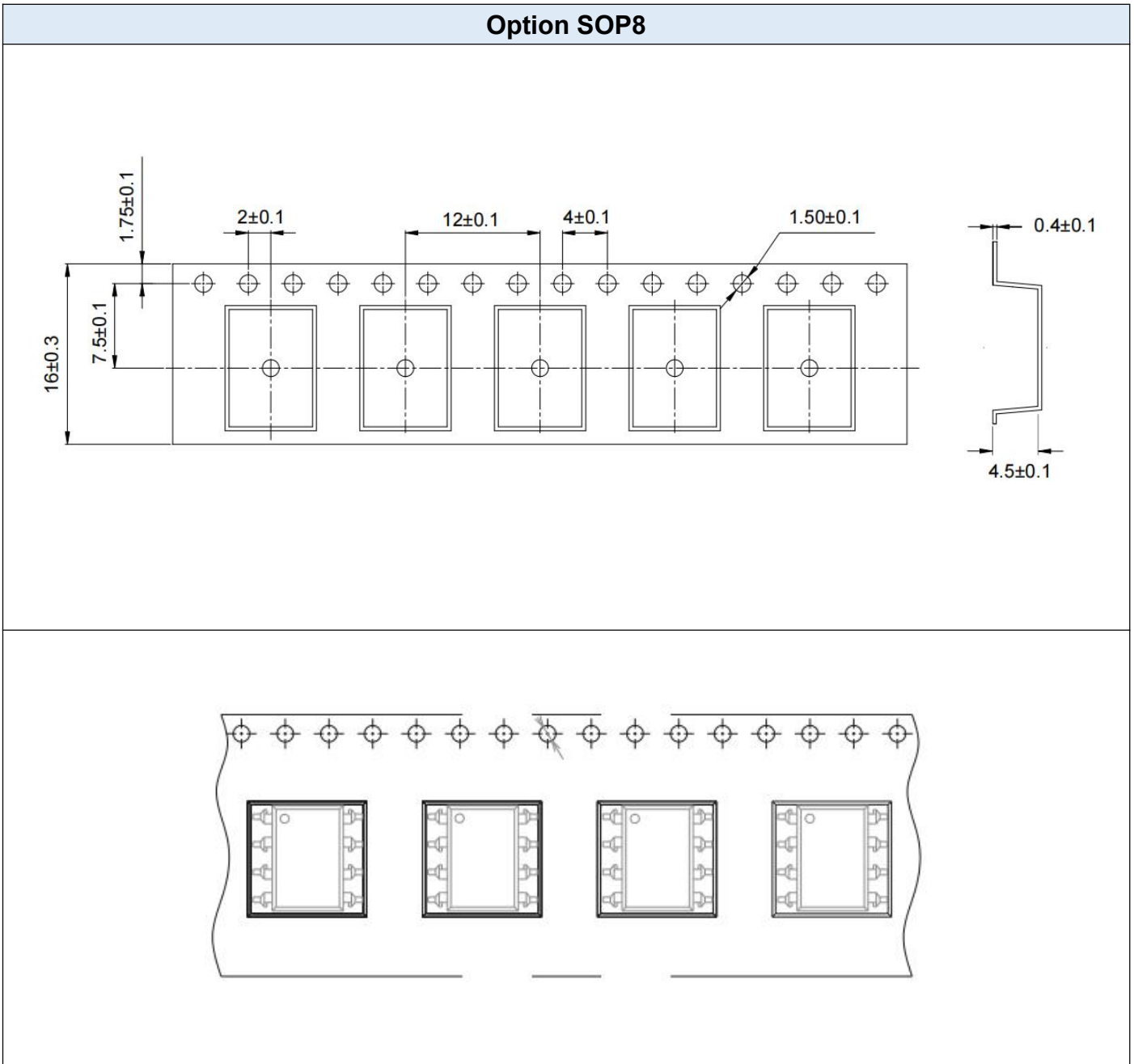
Surface Mount (Low Profile) Lead Forming



- Dimensions in mm unless otherwise stated

CARRIER TAPE SPECIFICATIONS

Option SOP8



- **Dimensions in mm unless otherwise stated**

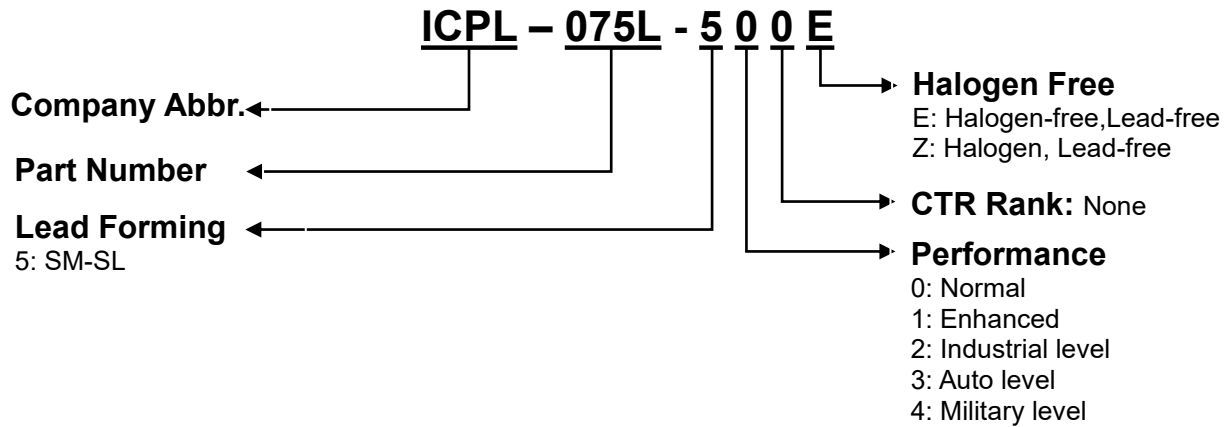
ORDERING AND MARKING INFORMATION

Marking Information



ICPL : Company Abbr.
075L : Part Number
/ : ISOMICRON
YY : Fiscal Year
WW : Work Week
B : Manufacturing Code

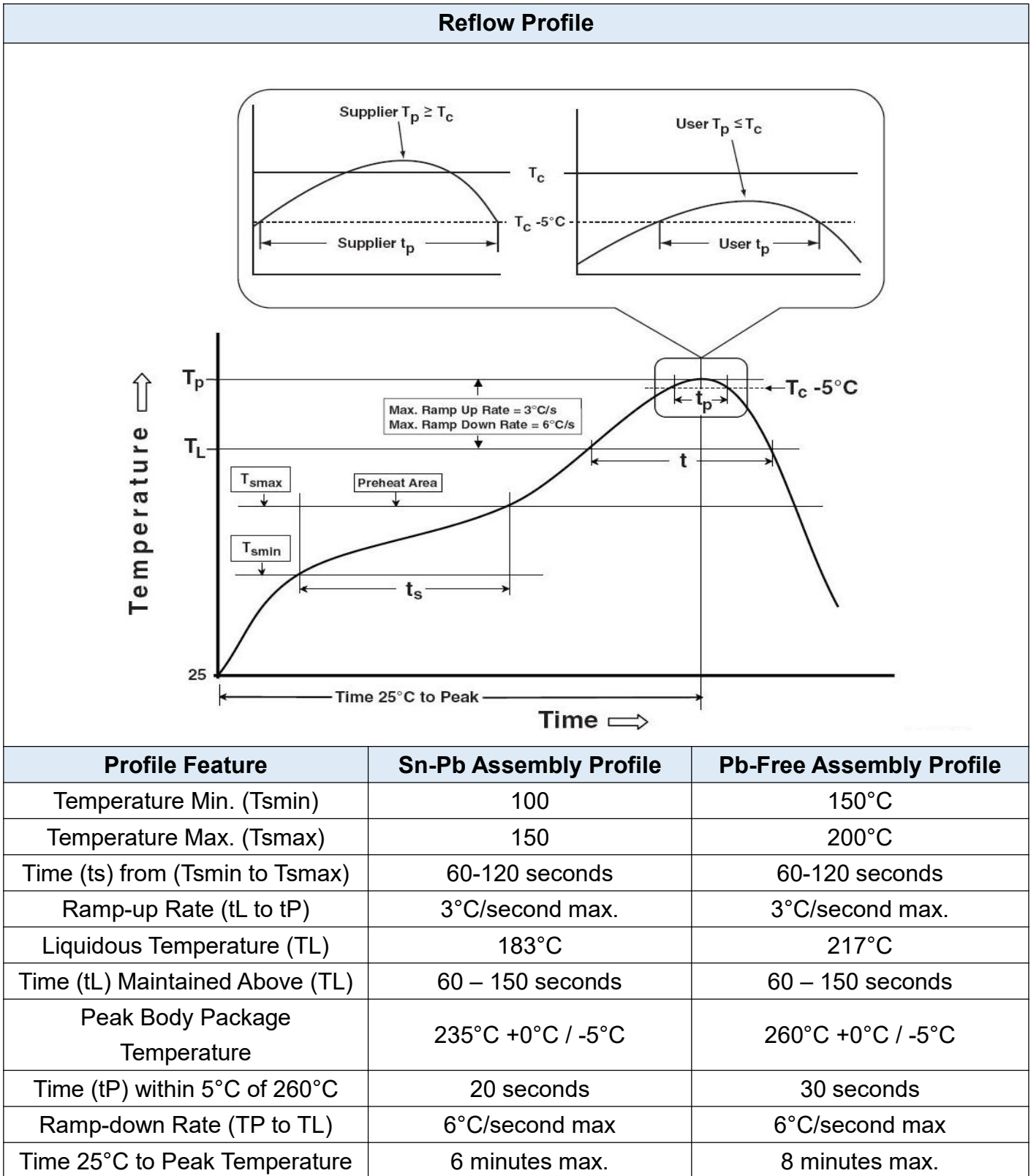
Order Code



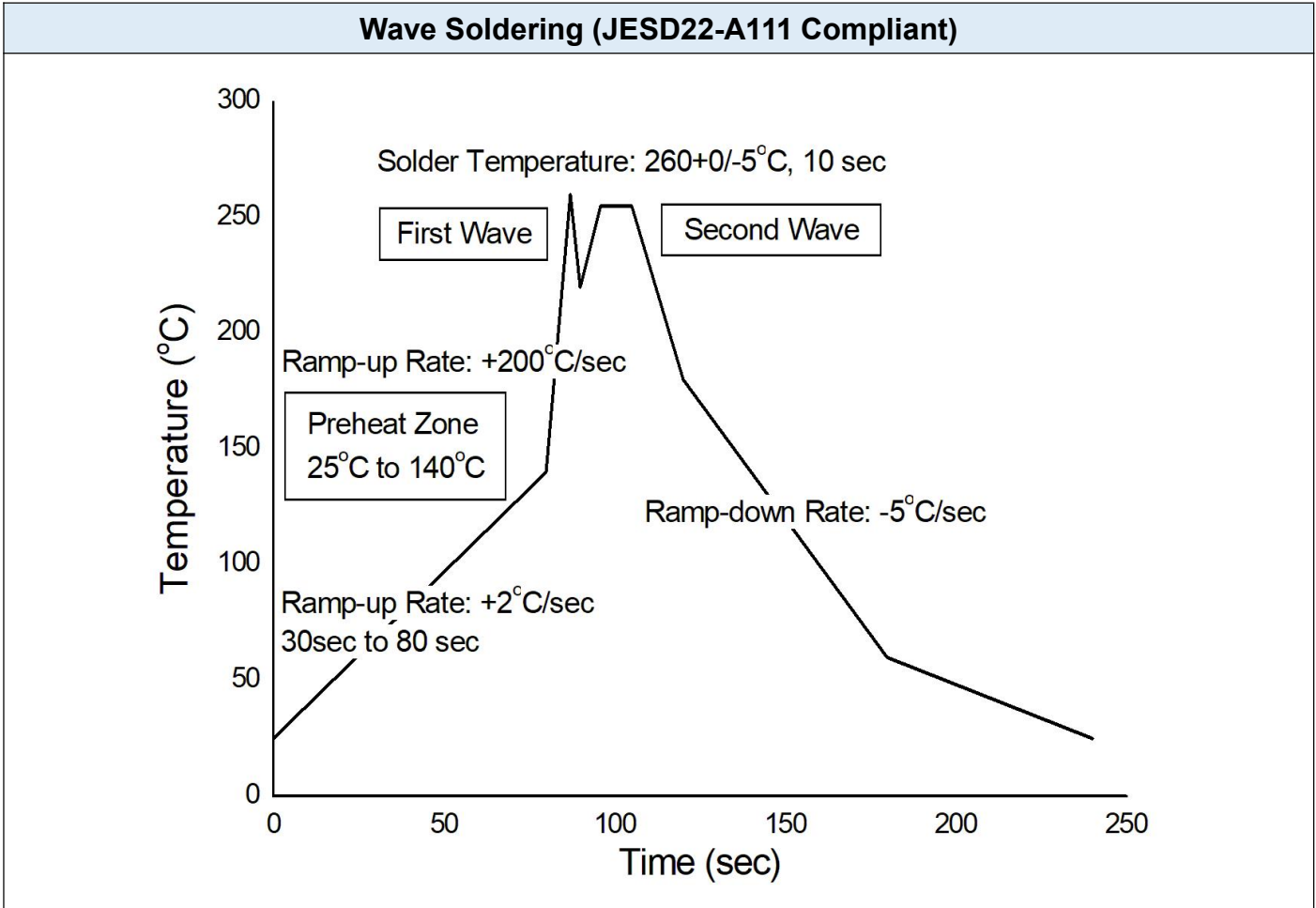
Packing Quantity

Option	Quantity	Quantity – Inner box	Quantity – Outer box
SM-SL	2000 Units/Reel	2 Reels/Inner box	5 Inner box/Outer box = 20k Units

REFLOW INFORMATION



TEMPERATURE PROFILE OF SOLDERING



Hand Soldering By Soldering Iron	
Soldering Temperature	380+0/-5°C
Soldering Time	3 sec max.

- One time soldering is recommended for all soldering method.
- Do not solder more than three times for IR reflow soldering.

DISCLAIMER

- ISOMICRON is continually improving the quality, reliability, function and design. ISOMICRON reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
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- Please contact ISOMICRON sales agent for special application request.
- Immerge unit's body in solder paste is not recommended.
- Parameters provided in datasheets may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated in each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify ISOMICRON's terms and conditions of purchase, including but not limited to the warranty expressed therein.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.